

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re Patent Application of:  
Regnier et al.

Application No.: 10/589,275

Confirmation No.: 5699

Filed: August 10, 2006

Art Unit: 2811

For: PROCESS FOR THE FABRICATING AN  
ELECTRONIC INTEGRATED CIRCUIT  
AND ELECTRONIC INTEGRATED  
CIRCUIT THUS OBTAINED

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Examiner: J. Im

**RESPONSE TO OFFICE ACTION**

MS AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the final Office Action dated May 26, 2010, please consider the following claims and remarks: